

Q.P. Code: 19MC9102

**R19**

Reg. No: 

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**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR**  
(AUTONOMOUS)

**MCA I Year I Semester Supplementary Examinations August-2021**

**COMPUTER ORGANIZATION**

**(MCA)**

Time: 3 hours

Max. Marks: 60

(Answer all Five Units 5 x 12 = 60 Marks)

**UNIT-I**

- 1 Draw the H/W Flowchart and H/W Algorithm for Multiplication for positive numbers with a suitable example. **12M**

**OR**

- 2 Explain about arithmetic operations on floating point numbers with its neat sketch. **12M**

**UNIT-II**

- 3 Discuss about the cache memory with different types of mappings. **12M**

**OR**

- 4 Explain about Micro Programmed Control with Micro Program Example diagram. **12M**

**UNIT-III**

- 5 a Explain about assembler directives. **6M**  
b Explain about Data transfer instructions. **6M**

**OR**

- 6 Explain about shift instructions with example. **12M**

**UNIT-IV**

- 7 What is DMA? Draw the block diagram for DMA controller and explain about DMA transfer in a computer. **12M**

**OR**

- 8 Explain about modes of transfer and its types. **12M**

**UNIT-V**

- 9 a Explain about Parallel Processing and its Types. **6M**  
b Explain the concept of Pipelining with clear example with neat sketch. **6M**

**OR**

- 10 a Explain about vector processing. **6M**  
b Explain about Array processors. **6M**

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